

L Number	Hits	Search Text	DB	Time stamp
1	2108	347/\$.ccls. and (CPU and (circuit blocks))	USPAT	2002/01/03 11:02
2	0	(347/\$.ccls. and (CPU and (circuit blocks))) and (init\$ same driv\$)	USPAT	2002/01/03 11:02
3	302	(347/\$.ccls. and (CPU and (circuit blocks))) and (init\$ same driv\$)	USPAT	2002/01/03 11:03
4	120	((347/\$.ccls. and (CPU and (circuit blocks))) and (init\$ same driv\$)) and enable	USPAT	2002/01/03 11:04
5	0	((((347/\$.ccls. and (CPU and (circuit blocks))) and (init\$ same driv\$)) and enable) and (stable operation))	USPAT	2002/01/03 11:23
6	48	((((347/\$.ccls. and (CPU and (circuit blocks))) and (init\$ same driv\$)) and enable) and (stable operation))	USPAT	2002/01/03 11:23
7	2700	327/\$.ccls. and (init\$ same (power on))	USPAT	2002/01/03 11:24
8	2454	(327/\$.ccls. and (init\$ same (power on))) and ((CPU or control\$) and (circuit blocks))	USPAT	2002/01/03 11:26
9	907	((327/\$.ccls. and (init\$ same (power on))) and ((CPU or control\$) and (circuit blocks))) and enable	USPAT	2002/01/03 11:27
10	900	((((327/\$.ccls. and (init\$ same (power on))) and ((CPU or control\$) and (circuit blocks))) and enable) and (one chip configuration) or monoli\$)	USPAT	2002/01/03 11:27
11	902	((((327/\$.ccls. and (init\$ same (power on))) and ((CPU or control\$) and (circuit blocks))) and enable) and ((one chip configuration) or monoli\$))	USPAT	2002/01/03 11:29
12	906	((((327/\$.ccls. and (init\$ same (power on))) and ((CPU or control\$) and (circuit blocks))) and enable) and (integrate circuit))	USPAT	2002/01/03 11:31
13	901	((((327/\$.ccls. and (init\$ same (power on))) and ((CPU or control\$) and (circuit blocks))) and enable) and (integrate circuit)) and (((327/\$.ccls. and (init\$ same (power on))) and ((CPU or control\$) and (circuit blocks))) and enable) and ((one chip configuration) or monoli\$))	USPAT	2002/01/03 11:29
14	906	((((327/\$.ccls. and (init\$ same (power on))) and ((CPU or control\$) and (circuit blocks))) and enable) and (semiconductor integrate circuit))	USPAT	2002/01/03 11:32
15	120	((((347/\$.ccls. and (CPU and (circuit blocks))) and (init\$ same driv\$)) and enable) and (stable operation))	USPAT	2002/01/03 11:33
16	48	((((347/\$.ccls. and (CPU and (circuit blocks))) and (init\$ same driv\$)) and enable) and (stable operation)) and (((347/\$.ccls. and (CPU and (circuit blocks))) and (init\$ same driv\$)) and enable) and (stable operation))	USPAT	2002/01/03 11:33

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CLIPPEDIMAGE= JP02001100873A
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TITLE: INITIAL SETTING PROCESSING CIRCUIT FOR LINE CARD

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ABSTRACT:

PROBLEM TO BE SOLVED: To provide an initial setting processing circuit for line card, for performing initial setting of a device inside a line card after connecting a main body and the line card via a connecting means.

SOLUTION: This circuit is composed of a first discriminating circuit 2 for outputting an initial setting start signal 1c for starting initial setting processing of the device inside the line card, corresponding to the AND condition of a signal 1f showing the power supply voltage of the line card is established and signals 1a and 1b which request initial processing of the line card, from a CPU in the main body, an initial setting circuit 3 for receiving the initial setting start signal 1c, performing initial setting processing of the device inside the line card and outputting an initialization success/failure signal 1d showing whether relevant initial setting processing has been successful or failing, and a second discriminating circuit 4 for outputting a setting end signal 1e to the CPU on the side of the main body, on

the basis of the initialization success/ failure signal.

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